

DEEP LEARNING ON-CHIP

SUMMER SCHOOL



SAVE THE DATE
September 20-22, 2017

Politecnico di Torino
Torino, ITALY



COVERED TOPICS

Deep Learning Theory and Software

Accelerating Deep Learning using
FPGAs and ASICs

CAD Tools for Deep Learning on-Chip

Cognitive Cyberphysical Systems

Brain-Inspired Electronics with
Emerging Technologies

Applications of Deep Learning for
Data-Analytics and Visual Reasoning

PLENARY TALKS

Luca Benini
ETH Zurich

Stefano Mattocchia
Università di Bologna

Damien Querlioz
CNRS Paris

Alessandro Sperduti
Università di Padova

Zhiru Zhang
Cornell University

CONTACTS

Andrea Calimera

Department of Control and Computer
Engineering

andrea.calimera@polito.it

Mario Casu

Department of Electronics and Telecommunication
Engineering

mario.casu@polito.it

subscribe to our mailing list at www.macloc.org